		BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB	RRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRR		
LLL	iii	BBB BBB	RRR RRR	TTT	
	!!!		RRR RRR	III	LLL
IIIIIIIIIIIIIII	111111111	BBBBBBBBBBBB	RRR RRR	III	rrrrrrrrrrr
LLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLL	111111111	888888888888	RRR RRR	III	LLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLL
LLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLLL	111111111	BBBBBBBBBBBB	RRR RRR	TTT	IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII

LI

	BBBBBBBB BBBBBBBB BB BB BB BB BB BB BBBBBB	MM MM MMM MMM MMMM MMMM MMM MM MM MM MM	UU	AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA
	\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$\$			

LIE

- Dummy substitute for LIBSEMULATE handl 16-SEP-1984 00:07:06 VAX/VMS Macro V04-00 LIBSEMULATE Table of contents 46 DECLARATIONS
LIBSEMULATE - Dummy handler replacement for LIBSEMULATE (2) (3)

LIE

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MA

- Dummy substitute for LIBSEMULATE handl 16-SEP-1984 00:07:06 VAX/VMS Macro V04-00 Page 1 (1)

.TITLE LIBSEMULATE - Dummy substitute for LIBSEMULATE handler .IDENT /2-001/ ; File: LIBEMULAT.MAR Edit: SBL1001

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FACILITY: General Utility Library

ABSTRACT:

This module contains the condition handler LIBSEMULATE, which only returns SSS_RESIGNAL.

ENVIRONMENT: Runs at any access mode, AST Reentrant

AUTHOR: Steven B. Lionel, CREATION DATE: 23-May-1983

MODIFIED BY:

2-001 - Replacement for LIBSEMULATE 1-012. SBL 23-May-1983

```
- Dummy substitute for LIBSEMULATE handl 16-SEP-1984 00:07:06 VAX/VMS Macro V04-00 DECLARATIONS 6-SEP-1984 11:06:28 [LIBRIL.SRC]LIBEMULAT.MAR;1
 .SBTTL DECLARATIONS
                     LIBRARY MACRO CALLS:
                             $SSDEF
                                                         ; SS$_ symbols
                     EXTERNAL DECLARATIONS:
                             .DSABL GBL
                                                         ; Force all external symbols to be declared
                     MACROS:
                            NONE
                     EQUATED SYMBOLS:
                            NONE
                     OWN STORAGE:
                            NONE
                     PSECT DECLARATIONS:
                            .PSECT _LIB$CODE PIC, USR, CON, REL, LCL, SHR, - EXE, RD, NOWRT, LONG
```

50

```
- Dummy substitute for LIBSEMULATE handl 16-SEP-1984 00:07:06 LIBSEMULATE - Dummy handler replacement 6-SEP-1984 11:06:28
                                                                                                                                         VAX/VMS Macro V04-00
[LIBRTL.SRC]LIBEMULAT.MAR;1
                                                               .SBTTL LIBSEMULATE - Dummy handler replacement for LIBSEMULATE
                                        777788888888888999999999990123456789
                                                   FUNCTIONAL DESCRIPTION:
                                                              This dummy condition handler, which only returns SS$ RESIGNAL, is a replacement for the LIB$EMULATE floating-point emulation handler which was supplied in VAX/VMS V3.0. Since emulation of floating point instructions is now handled automatically by VAX/VMS, (see LEMULAT.SRC]FPEMULATE.MAR), user programs no longer need to establish LIB$EMULATE as a handler. However, so that existing programs will continue to link properly, this dummy handler replaces LIB$EMULATE; it only returns SS$ RESIGNAL. Since the SS$ OPCDEC exception from missing floating instructions will never be seen by user programs, this handler is a no-op.
                                                               Programs which reference LIB$EMULATE can now remove any such
                                                               references.
                                                   CALLING SEQUENCE:
                                                               ret_status.wlc.v = LIB$EMULATE (signal_args.rr.r, mch_args.rr.r)
                                                   FORMAL PARAMETERS:
                                                               signal_args
                                                                                             The signal arguments list
                                                               mch_args
                                                                                             The mechanism arguments list
                                                   IMPLICIT INPUTS:
                                                               NONE
                                                   IMPLICIT OUTPUTS:
                                                               NONE
                                                   COMPLETION STATUS:
                                                               SS$_RESIGNAL - Resignal condition to next handler
                                                   SIDE EFFECTS:
                                                               NONE
               0000
                                                               .ENTRY LIBSEMULATE, ^M<>
0918 8F
                                                                                                                               Resignal condition to next handler
                                                               MOVZWL #SS$_RESIGNAL, RO
                                                               RET
                                                                                                                            : End of routine LIBSEMULATE
```

: End of module LIBSEMULATE

.END

(3)

```
1-
```

```
- Dummy substitute for LIBSEMULATE handl 16-SEP-1984 00:07:06 6-SEP-1984 11:06:28
                                                                                                                                      VAX/VMS Macro V04-00 [LIBRTL.SRC]LIBEMULAT.MAR; 1
LIBSEMULATE
                                                                                                                                                                               Page
Symbol table
LIBSEMULATE
SSS_RESIGNAL
                                            = 00000000 RG
                                                                    02
                                                                       Psect synopsis
PSECT name
                                             Allocation
                                                                          PSECT No.
                                                                                         Attributes
                                             00000000
                                                                                                                                            NOEXE NORD
EXE RD
EXE RD
                                                                                                                                                             NOWRT NOVEC BYTE NOWRT NOVEC LONG
    ABS
                                                                                  0.)
                                                                                         NOPIC
                                                                                                     USR
                                                                                         NOPIC
$ABS$
                                                                                                     USR
                                                                                                              CON
                                                                                                                      ABS
                                                                                                                              LCL NOSHR
_LIB$CODE
                                              8000000
                                                                                                     USR
                                                                                                              CON
                                                                                                                                       SHR
                                                                  Performance indicators
                                                                              Elapsed Time
Phase
                                   Page faults
                                                         CPU Time
                                                                              00:00:02.23
00:00:03.12
00:00:10.65
00:00:01.19
00:00:02.12
00:00:00.01
00:00:00.02
                                                        00:00:00.04
00:00:00.34
00:00:02.39
00:00:00.41
Initialization
Command processing
Pass 1
                                               37
Symbol table sort
                                                        00:00:00.46
00:00:00.01
00:00:00.02
Pass 2
Symbol table output
Psect synopsis output
Cross-reference output
Assembler run totals
```

The working set limit was 1200 pages.
19494 bytes (39 pages) of virtual memory were used to buffer the intermediate code.
There were 30 pages of symbol table space allocated to hold 408 non-local and 0 local symbols.
126 source lines were read in Pass 1, producing 12 object records in Pass 2.
8 pages of virtual memory were used to define 7 macros.

Macro library statistics !

Macro Library name

Macros defined

_\$255\$DUA28:[SYSLIB]STARLET.MLB;2

469 GETS were required to define 4 macros.

There were no errors, warnings or information messages.

MACRO/ENABLE=SUPPRESSION/DISABLE=(GLOBAL, TRACEBACK)/LIS=LIS\$:LIBEMULAT/OBJ=OBJ\$:LIBEMULAT MSRC\$:LIBEMULAT/UPDATE=(ENH\$:LIBEMULAT)

0206 AH-BT13A-SE

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